

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING  
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL**

Date : 7<sup>th</sup> June 2016

**List of Applicants Short-listed for Written Test (and on qualifying to Interview)  
for M.Tech (R ) admission (June 2016 Session)**

**Applicants should see the instruction below (immediately following this list)**

Sl. No.	Application No.	Name of the applicant	Remarks
1	PS01	Sharadhi S S	
2	PS02	Sambit Dash	
3	PS03	Aditiya Shankar Kar	
4	PS04	Shuvam Sarkar Roy	
5	PS05	Gautam Singh Sardar	
6	PS06	Adrita Santra	No stamps & envelope #
7	PS01(Spons)	Binesh Mohan P	
8	PS07(Non Scholarship)	Adrita Santra	No stamps & envelope #

**# These applicants will not be sent hard copy call letter by post as they have not given the self-addressed envelopes with appropriate postal-stamps.**

Call letter along with Instructions to Candidates has been sent to the applicants to their correspondence address by speed post.

Go through the Instructions to the candidates published in the department website ([www.eee.nitk.ac.in](http://www.eee.nitk.ac.in) ; given below) carefully and adhere to the instructions.

For any clarifications contact Dr. C.M.C.Krishnan, Secretary, DPGC, through e-mail: [cmckrishnan@nitk.edu.in](mailto:cmckrishnan@nitk.edu.in) or phone : 09535647422.

HOD (E & E)

<b>Department</b>	: Electrical and Electronics
<b>Place of Written test &amp; Interview</b>	: Dept. of Electrical and Electronics Engg Western Side Campus, NITK, Surathkal
<b>Registration</b>	: 23-06-2016, 08:00 AM
<b>Written Test</b>	: 23-06-2016, 08:30 AM to 11:30 AM
<b>Announcement of Shortlisted Candidates for Interview*</b>	: 23-06-2016, 05.30 PM
<b>*(provisional; pending document verification)</b>	
<b>Document Verification</b>	: 23-06-2016, 03:00 PM at Seminar Hall, Dept. of E & E
<b>Interview</b>	: 24-06-2016, 8.30 AM onwards at Conference Room, Dept. of E & E

**PROFESSOR AND HEAD**

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING  
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA  
SRINIVASNAGAR, SURATHKAL, MANGALORE - 575 025, INDIA

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING  
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL,  
SRINIVASNAGAR, MANGALORE – 575 025**

Tel : 0824-2473045, 2474054

Fax : 0824-2474039

Website : www.eee.nitk.ac.in

No.: NITK/EED/PhD/2016-17/06/07/ **2397**

Date : 07-June-2016

**OFFICIAL MEMORANDUM**

Sub: M.Tech(R) Programme - Selection / Admission for the Year 2016-17 Intimation for  
Written Test and Interview.

To : Mr/ Ms. \_\_\_\_\_

Application No: \_\_\_\_\_

With reference to your application for admission to M.Tech(R) Programme in the Department of Electrical and Electronics Engg., you are requested to appear for a written test and interview before the selection committee (at your own cost). You should produce all records such as Date of Birth Certificate, Degree Certificate Marks Cards of all years/semesters (U.G. Programme), GATE Score Card, Class 10<sup>th</sup> & 12<sup>th</sup>, Diploma Marks Card, Sponsorship certificate (if applicable), SC/ST Certificate (if applicable), OBC Certificate issued on or after 01 April 2016 (if applicable), Persons with Disabilities (PWD) certificate (if applicable), Conduct certificate and Testimonials, and valid photo identity card, **(All these originals)**. Please bring an attested photocopy of each of these certificates to submit at the time of interview.

<b>Department</b>	: Electrical and Electronics
<b>Place of Written test &amp; Interview</b>	: Dept. of Electrical and Electronics Engg., Western Side Campus, NITK, Surathkal
<b>Registration</b>	: 23-06-2016, 08:00 AM
<b>Written Test</b>	: 23-06-2016, 08:30 AM to 11:30 AM
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<b>Interview</b>	: 24-06-2016, 8.30 AM onwards at Conference Room, Dept. of E & E

**Category** : Full time/Part time

Note :

1. Please see the institute eligibility criteria given in the advertisement, carefully. It is the sole responsibility of candidate to ascertain his/her eligibility before coming for the interview. The final confirmation of your eligibility is done only after the verification of original certificates.
2. Candidates should come prepared to appear for a written Research-Aptitude Test before the interview. The Candidates are required to go through the list of Instructions to Candidates which is enclosed herewith.
3. Candidates must bring the **"Data Collection Format"** (enclosed) **duly filled** and submit at the time of registration (8.00 am) on 23<sup>th</sup> June 2016.
4. Sponsored candidates (Full time/Part time) should have been serving in the sponsoring organization for a period of at least 2 years after qualifying degree and have to produce a letter from their employer stating that the candidate is deputed for Research Programme (Full time / Part time) of the Institute on **full salary**, during the study period. The employer should indicate that the candidate will not be withdrawn midway before the completion of the course. (Sponsorship letter should be in format provided in the Application Form).
5. **CANDIDATES WHO HAVE NOT SUBMITTED MARKS OF FINAL EXAMINATION ALONG WITH APPLICATION FORM SHALL PRODUCE THE SAME AT THE TIME OF ADMISSION. HOWEVER, CANDIDATES WHO HAVE WRITTEN FINAL YEAR EXAMINATIONS AND ARE YET TO OBTAIN FINAL SEMESTER MARK CARDS SHOULD SUBMIT THE SAME ON OR BEFORE 15.09.2016.**
6. Candidates who have qualified from the other Universities (Other than NITK) have to produce Migration certificate in order to validate their admission.
7. **The selected candidates are required to pay fees and get admitted during the period 07.07.2016 to 14.07.2016. Fee details are as given in the "Information Bulletin 2016-17 (M.Tech(Research) and PhD)". Given overleaf.**

  
**HEAD OF THE DEPARTMENT**  
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING  
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA  
SRINIVASNAGAR, SURATHKAL, MANGALORE - 575 025, INDIA

**Fee Structure - M.Tech Sponsored/M.Tech Research**  
(At the time of Admission)

The candidates are directed to bring D.D. (1) in favour of Director, N.I.T.K. Surathkal (For Institute fee) and (2) Professor in- charge, NITK Hostel Affairs, Surathkal (For Hostel fee) payable at S.B.I., Surathkal OR Canara Bank, Mangalore

<p style="text-align: center;"><b><u>At N.I.T.K. Office</u></b> (in favour of Director, N.I.T.K., Surathkal)</p>			<p style="text-align: center;"><b><u>At Hostel Office</u></b> (in favour of Professor in-charge, NITK Hostel Affairs, Surathkal)</p>
Category	Hosteller	Day Scholar	All Students who require Hostel
G M & OBC	58,825.00	54,325.00	17,900.00
SC/ST	23,825.00	19,325.00	
External Registrants GM & OBC	58,475.00	53,975.00	
External Registrants SC/ST	58,475.00	53,975.00	

**NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA-SURATHKAL**  
**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**

Date : 7<sup>th</sup> June 2016

**INSTRUCTIONS TO CANDIDATES APPLIED FOR M.Tech.(R)**

Dear Candidates,

You are invited to attend the interview for the M.Tech.(R) programme in our department. Here are few more instructions to make it convenient to all of us.

1. The following Certificates shall be brought in ORIGINAL for verification purposes.
  - (a) GATE Score Card
  - (b) B.E / B.Tech Marks Cards and Degree Certificates
  - (c) X and XII marks cards
  - (d) Caste Certificate (if applicable)
  - (e) Valid proof of photo identity.(Note : For OBC, the certificate must be dated on or after 1<sup>st</sup> April 2016. Please make sure that the caste name appear in the central list published by National Commission of Backward Classes (NCBC) Govt. of India at the link : [www.ncbc.nic.in](http://www.ncbc.nic.in). **If the caste is not listed by NCBC or the certificate date is prior to 1<sup>st</sup> April 2016, then the OBC candidate will be considered under Open Category Only.** The OBC certificate format is available through the department website [www.eee.nitk.ac.in](http://www.eee.nitk.ac.in))
2. Syllabus for Written Aptitude Test and Interview will be corresponding to the undergraduate programme of Dept. of Electrical and Electronics, NITK, Surathkal. The details of the courses and contents are available through the department website : [www.eee.nitk.ac.in](http://www.eee.nitk.ac.in).
3. Please visit the Department Website ([www.eee.nitk.ac.in](http://www.eee.nitk.ac.in)) for updates and instructions which will be released by the department from time to time. The Department will not entertain any such concerns by the candidates regarding unawareness of the updates and instructions published in the department website.
4. Non-Production of Original Documents for Verification Purposes will disqualify the candidate from selection process.
5. The procedure for selection is given overleaf. The Candidates are required to go through the procedure carefully before appearing for the selection process.
6. The candidates are informed to download and fill up the Data Collection Format carefully and submit during Document Verification. This data will be used for preparation of the merit list.
7. The Candidates are required to produce reprints of their publications, original certificates of work-experience and training-undergone at the time of Interview.
8. The Candidates are required to be available for the two days (23<sup>rd</sup> and 24<sup>th</sup> June 2016) at Dept. of E & E, NITK, Surathkal for completion of the selection process and be available on mobile call for receiving and providing necessary information's.
9. The candidates have to comply with the instructions published in the information bulletin for Ph.D & M.Tech ( R) Programme 2016-17, regarding admission procedure as stipulated by the Institute.
10. For any clarifications contact Dr. C.M.C.Krishnan, Secretary, DPGC, through e-mail: [cmckrishnan@nitk.edu.in](mailto:cmckrishnan@nitk.edu.in) or phone : 09535647422.

WISH YOU GOOD LUCK.

(Dr Vinatha U, HoD & Chairperson DPGC, EED)

[P.T.O]

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NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA  
SURATHKAL, MANGALORE - 575 025, INDIA



## Selection Procedure:

**Factors considered for selection:** The selection of the scholars is based on all the following components considered at different stages as indicated in the *Procedure of Selection*:

1. Application in the prescribed format with all necessary data and submission of further information/ data sought by the department at the time of registration and interview.
2. Performance at the Undergraduate level up to seventh semester.
3. GATE Score.
4. Performance in the written test.
5. Performance in the interview.
6. Experience and publications to be assessed during the interview.

**Procedure of Selection (at the department level):** The selection will involve **three stages**.

**Stage-1:** All eligible candidates will be allowed to compete in the written test. A short-list of candidates for interview will be formed by considering the performance at the UG, GATE Score and the written test. Cut-off will be decided considering the number of seats permitted by the institute or the capacity of the department whichever is lower and applying a discount for different categories (OBC, SC, ST). This is detailed in the *Procedure of short-listing for Interview*.

*Note: The written test will be in the morning session on 23<sup>rd</sup> June 2016 starting at 08:30 AM. Late entries are not allowed. Those who miss this scheduled-test will not be further considered for the selection process.*

**Stage-2:** The short-listed Candidates in Stage-1, will be Interviewed by the Departmental Post Graduate Committee (DPGC). The DPGC will prepare a list of candidates recommending for the M.Tech ( R ) programme based on the performance in the interview, experience, and publications. This list will be submitted to the Head of the Department. This may be a NULL list in case no candidate is found suitable.

**Stage-3:** The Head of the Department will allocate the candidates to the Faculty members by consulting the candidates and the Faculty members intending to take the M.Tech ( R ) scholars, and forward the recommendation to the Dean (A).

### **Procedure of short-listing of candidates for Interview:**

**Step-1:** All the Candidates meeting the institute eligibility criteria will be called for participating in the selection process.

**Step-2:** A written test will be conducted on the first day of the process to get a departmental level assessment of the candidates.

**Step-3:** A merit order will be formed for short-listing of candidates for interview giving weightages for different components as follows:

Undergraduate performance (First to Seven semesters)	GATE Score	Written test	Total
10 %	70 %	20 %	100 %

**Step-4:** If the department has N number of seats in "Open Category (OC)", then the cut-off Rank (R) for OC will be "3 times N" in the merit order formed in Step-3 OR 25% whichever is higher. This list (L1) may be represented by the candidates from all categories (OC, OBC, SC, ST) and has a Cut-off mark M1 corresponding to the Rank R. Note: In case of sponsored candidates (with minimum of 2 years' experience; with sponsorship letter as per regulation) a weightage of 15 % will be added in place of GATE weightage.

**Step-5:** The cut-off mark (M2) for OBC candidates is fixed at 90% of the Cut-off mark (M1) for OC. This list is L2 and contains all OBC candidates having score between and including M1 and M2 but limited by 3 times the number seats available in OBC category.

**Step-6:** The cut-off mark (M3) for SC candidates is fixed at 75% of the Cut-off mark (M1) for OC. This list is L3 and contains all SC candidates having score between and including M1 and M3 but limited by 3 times the number seats available in SC category.

**Step-7:** The cut-off mark (M4) for ST candidates is fixed at 70% of the Cut-off mark (M1) for OC. This list is L4 and contains all ST candidates having score between and including M1 and M4 but limited by 3 times the number seats available in ST category.

**Step-8:** The Shortlist of candidates for the interview is  $L = L1 + L2 + L3 + L4$ .

(Dr Vinatha U, HoD & Chairperson DPGC, EED)

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**NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA-SURATHKAL**  
**ELECTRICAL ENGINEERING**  
**M.Tech(EE) Data Collection format**  
**Admission to Programme for the Academic Session July-Dec. 2016**

<b>Name of the candidate</b>		<b>Application No/Registration No.</b>			
<b>Email id:</b>		<b>Mobile No.</b>			
<b>Under Graduate</b>	Awarded Marks / Grades		Equivalent % age marks (A=)		
	UG (Up to 7 semesters)	CGPA(C3G)	(C3G-0.5)*10		
		C3=%age	C3		
	UG (Aggregate of all semesters)	CGPA(C4G)	(C4G-0.5)*10		
		C4=%age	C4		
<b>GATE(* Declared out of 100 in GATE score card)</b>		Year	Marks(*)		Marks Normalized (*)
<b>10th Level (CGPA/ Marks)</b>		<b>12th level (CGPA/ Marks)</b>			
<b>Diploma (CGPA/ Marks)</b>					
<b>Remarks (≥ 6.5CGPA/60% for OC/OBC) (≥6.0CGPA/55% for SC/ST) @ PG level</b>		<b>QUALIFIED</b>		<b>NOT QUALIFIED</b>	
<b>Experience and Publications to be considered during the interview</b>					
<b>Experience (in years)</b>	Teaching	Industry	Others (Specify)		Remarks
<b>Publications (Numbers)</b>	Journals	Conferences	Books		Remarks

Declaration by the Candidate: I hereby state that I have understood all the instructions provided so far and that I have taken all the necessary care in providing the information in the Data Collection Form. I will be solely responsible for all the consequences arising out of submission of incorrect data, the one that is being used in the selection process.

Signature of the candidate  
Date: 23-06-2016  
Place: NITK, Dept. of EE

Verified by: Signature  
Name:  
Designation: Faculty Member/ Staff

**NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA-SURATHKAL**  
**ELECTRICAL ENGINEERING**

**Mtech(E) Data Collection format**

**Admission to : Programme for the Academic Session July-Dec. 2016**

**UG (Semesters 1 to 4) (First attempt Grades/Marks only to be used)**

Name of the candidate							Application No.						
Semester	Course Details			Result			Semester	Course Details			Result		
	Course code	Credits	Max. Marks/	Grade	Point	Marks		Course code	Credits	Max. Marks/	Grade	Point	Marks
1							2						
3							4						

Signature of the candidate

Date: 23-06-2016

Place: NITK, Dept. of EE

Verified by: Signature

Name:

Designation: Faculty Member/ Staff

**NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA-SURATHKAL  
ELECTRICAL ENGINEERING**

*M.Tech(R)*

**Data Collection format**

**Admission to Programme for the Academic Session July-Dec. 2016**

**UG (Semesters 5 to 8) (First attempt Grades/ Marks only to be used)**

Name of the candidate							Application No.						
Semester	Course Details			Result			Semester	Course Details			Result		
	Course code	Credits	Max. Marks/	Grade	Point	Marks		Course code	Credits	Max. Marks/	Grade	Point	Marks
5							6						
7							8						
CGPA (in case of Grades) upto 7th Semester			C3G			CGPA (in case of Grades) upto 8th Semester			C4G				
Percentage (in case of Marks) upto 7th Semester			C3			Percentage (in case of Marks) upto 8th Semester			C4				

Signature of the candidate

Date: 23-06-2016

Place: NITK, Dept. of EE

Verified by: Signature

Name:

Designation: Faculty Member/ Staff